



Receipt

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor : TAKANORI IWAMATSU, ET AL. #4  
Serial No : 09/771,229  
Filed : January 26, 2001  
Title : CLOCK PHASE DETECTING CIRCUIT AND CLOCK...  
Group Art Unit : 2631

August 1, 2001

Attn: Customer Corrections Division  
Assistant Commissioner for Patents  
Washington, D.C. 20231

**REQUEST FOR CORRECTED FILING RECEIPT**

SIR:

We received the filing receipt on the above-referenced case, copy enclosed, wherein the ENTITY was stated as **SMALL ENTITY**. Please change the same to read: **LARGE ENTITY**. Also, the **TITLE** was incorrectly listed as: **CLOCK PHASE DETECTING CIRCUIT AND CLOCK REGENERATING CIRCUIT EACH ARRNAGED IN RECEIVING UNIT OF MULTIPLEX RADIO EQUIPMENT**. Please change the same to read: **CLOCK PHASE DETECTING CIRCUIT AND CLOCK REGENERATING CIRCUIT EACH ARRANGED IN RECEIVING UNIT OF MULTIPLEX RADION EQUIPMENT**.

Any fee, due as a result of this paper not fully covered by an enclosed check, may be charged on Deposit Account No. 08-1634.

Respectfully submitted,

Samson Helfgott  
Reg. No. 23,072

Helfgott & Karas, P.C.  
60th Floor  
Empire State Building  
New York, New York 10118  
Tel. (212) 643-5000  
Docket No.:13.045A  
SH:eju:FILREC

Any fee due with this paper, not fully served by an enclosed check, may be charged on deposit Acct. No. 08-1634

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: COMMISSIONER OF PATENTS AND TRADEMARKS, WASHINGTON, D.C. 20231, ON THE DATE INDICATED BELOW.

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DATE August 1, 2001



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HELFGOTT &amp; KARAS

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 WASHINGTON, D.C. 20231  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/771,229	01/26/2001	2631	1806	FUJS 13.045A	61	47	9

## CONFIRMATION NO. 6938

## UPDATED FILING RECEIPT



\*OC00000006182255\*

Helfgott & Karas  
 350 Fifth Avenue Suite 6024  
 Empire State Building  
 New York, NY 10118

Date Mailed: 06/14/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Takanori Iwamatsu, Kawasaki, JAPAN;  
 Hiroyuki Kiyanagi, Sendai, JAPAN;

## Domestic Priority data as claimed by applicant

THIS APPLICATION IS A REI OF 08/552,543 11/03/1995 PAT 5,867,542

## Foreign Applications

JAPAN HEI 7-059377 03/17/1995

If Required, Foreign Filing License Granted 03/07/2001

Projected Publication Date: N/A

Non-Publication Request: No

Early Publication Request: No

\*\*SMALL ENTITY\*\* \* \* LARGE ENTITY \* \*

Title

ARRANGED

Clock phase detecting circuit and clock regenerating circuit each damaged in receiving unit of multiplex radio equipment

Preliminary Class

375

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Data entry by : CHADWICK, YOLANDA

Team : 1600

Date: 06/14/2001

